

Friday, August 6, 2004

7:00 PM – 9:00 PM Evening Registration (Ballroom Level)

Except Saturday reception & dinner banquet in Sierra Ballroom, all workshop events take place in the Golden State Room.

Saturday, August 7, 2004

- 07:30am – 08:30am **Registration**
- 08:30am – 08:45am **Welcome & Introduction**
Anselmo Lastra, Ming Lin and Dinesh Manocha
(University of North Carolina at Chapel Hill)
- 08:45am – 09:25am ***On the Power of Streaming Table-Lookup***
Frederick P. Brooks, Jr. (University of North Carolina at Chapel Hill)
- 09:25am – 10:55am **BREAK & POSTER PRESENTATIONS (I)**
- 10:55am – 11:35am ***Stream Programming Environments***
Pat Hanrahan (Stanford University)
- 11:35am – 12:15am ***Compiler Support for GPUs: Challenges, Obstacles, and Opportunities***
Keith Cooper (Rice University)
- 12:15pm – 01:45pm **LUNCH**
- 01:45pm – 02:25pm ***GPUs: Economic Attraction and Performance Challenges***
Dan Reed (University of North Carolina at Chapel Hill)
- 02:25pm – 03:05pm ***GPU Requirements for Large Scale Scientific Applications***
Mark Seager (Lawrence Livermore National Lab)
- 03:05pm – 04:35pm **BREAK & POSTER PRESENTATIONS (II)**
- 04:35pm – 05:15pm ***Using GPUs as CPUs for Engineering Applications: Challenges and Issues***
Michael Heroux (Sandia National Laboratories)

- 05:15pm – 05:55pm** ***Utilizing Commercial Graphics Processors in the Real-time Geo-Registration of Streaming High-Resolution Imagery***
Laurence Flath (Lawrence Livermore National Lab)
- 05:55pm – 06:00pm** **Announcements**
- 06:30pm - 09:00pm** **RECEPTION & DINNER BANQUET (Sierra Ballroom)**

Sunday, August 8, 2004

- 07:45am – 08:30am** **Registration**
- 08:25am – 08:30am** **Announcements**
- 08:30am – 09:10am** ***Graphics Memory for General Applications***
Turner Whitted (Microsoft Research)
- 09:10am – 10:40am** **BREAK & POSTER PRESENTATIONS (III)**
- 10:40am – 11:20am** ***The Case for Asymmetric Multiprocessor Architecture***
Kai Li (Princeton University)
- 11:20am – 12:00pm** ***The All-Purpose Unit (APU) based on a Tiled-Processor Architecture: A Logical Successor to the CPU, GPU, and NPU?***
Anant Agarwal (Massachusetts Institute of Technology)
- 12:00pm – 01:30pm** **LUNCH**
- 01:30pm – 02:10pm** ***Stream Processors vs. GPUs***
Bill Dally (Stanford University)
- 02:10pm – 03:40pm** **BREAK & POSTER PRESENTATIONS (IV)**
- 03:40pm – 05:15pm** **Panel on “*GPUs and CPUs: The Uneasy Alliance?*”**
Moderator: Peter N. Glaskowsky (MicroDesign Resources)
Panelists: Mike Doggett (ATI Technologies)
Dave Kirk (NVIDIA)
Adam Lake (Intel Corporation)
Bill Mark (University of Texas at Austin)
Neil Trevett (3Dlabs)
- 05:15pm – 05:20pm** **Best Student Poster Award**
- 05:20pm - 05:30pm** **Closing Remarks**