Global DM schedulability analysis: upper and lower bounds*

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Abstract

A new technique was recently introduced [7] for the analysis of real-time systems scheduled on multiprocessor platforms by the global Earliest Deadline First (EDF) scheduling algorithm. In this paper, this technique is adapted and applied to the schedulability analysis of real-time systems scheduled on multiprocessor platforms by the global Deadline-Monotonic (DM) scheduling algorithm. It is shown that the resulting analysis technique is quantitatively superior to pre-existing DM schedulability analysis tests; in addition, the degree of its deviation from any hypothetical optimal scheduler (that may be clairvoyant) is also quantitatively bounded.

Keywords: Global multiprocessor scheduling; sporadic tasks; Deadline Monotonic; schedulability analysis; processor speedup factor

1 Introduction

Real-time systems comprised of recurrent processes or tasks are often represented using the sporadic task model [11, 5]. The Deadline-Monotonic (DM) [10] scheduling algorithm is widely used for scheduling such task systems on uniprocessor platforms. Recently, work has been done on extending DM to apply to multiprocessor platforms as well; such work addresses both implementation and analysis issues.

In this paper, we study the DM scheduling of sporadic task systems on preemptive multiprocessor platforms, under the assumption that inter-processor migration is permitted and incurs no penalty. We derive a new sufficient schedulability test, and quantify the “goodness” of this test by deriving its processor speedup factor. We also prove a lower bound on the processor speedup factor, thereby bounding the degree by which our test deviates from an optimal one.

We describe the problem in greater detail during the remainder of this section.

Task Model. A real-time system is often modeled as a finite collection of independent recurring tasks, each of which generates a potentially infinite sequence of jobs. Each job is characterized by an arrival time, an execution requirement, and a deadline, and it is required that a job complete execution between its arrival time and its deadline. Different formal models for recurring tasks place different restrictions on the values of the parameters of jobs generated by each task. One of the more commonly used formal models is the sporadic task model [11, 5]. A sporadic task τᵢ = (Cᵢ, Dᵢ, Tᵢ) is characterized by a worst-case execution requirement Cᵢ, a (relative) deadline Dᵢ, and a minimum inter-arrival separation parameter Tᵢ, also referred to as the period of the task. Such a sporadic task generates a potentially infinite sequence of jobs, with successive job-arrivals separated by at least Tᵢ time units. Each job has a worst-case execution requirement equal to Cᵢ and a deadline that occurs Dᵢ time units after its arrival time. We refer to the interval, of size Dᵢ, between such a job’s arrival instant and deadline as its scheduling window. A sporadic task system is comprised of several such sporadic tasks. Let τ denote a system of such sporadic tasks: τ = {τ₁, τ₂, . . . , τₙ}, with τᵢ = (Cᵢ, Dᵢ, Tᵢ) for all i, 1 ≤ i ≤ n. Task system τ is said to be a constrained sporadic task system if it is guaranteed that each task τᵢ ∈ τ has its relative deadline parameter no larger than its period: Dᵢ ≤ Tᵢ for all τᵢ ∈ τ. We restrict our attention here to constrained task systems. For notational convenience, we assume tasks are indexed in order of non-decreasing relative deadline parameters: Dᵢ ≤ Dᵢ₊₁ for all i, 1 ≤ i < n.

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Processor Model. We consider a multiprocessor platform that is comprised of several identical processors. We assume that the platform is fully preemptive — an executing job may be interrupted at any instant in time and have its execution resumed later with no cost or penalty. We also assume that the platform allows for global inter-processor migration – a job may begin execution on any processor and a preempted job may resume execution on the same processor as, or a different processor from, the one it had been executing on prior to preemption. (However, each job may execute on at most one processor at each instant in time.)

Deadline Monotonic scheduling. Priority-driven scheduling algorithms operate as follows: at each instant in time they assign a priority to each job that is awaiting execution, and choose for execution the jobs with the greatest priority. The Deadline Monotonic (DM) scheduling algorithm [10] is a priority-driven scheduling algorithm that assigns priority to jobs according to the relative-deadline parameter of the task that generates them — the smaller the relative deadline, the greater the priority — with ties broken arbitrarily but consistently. (Since we assume here that tasks are indexed in order of non-decreasing deadlines, this means that DM assigns jobs of $\tau_i$ priority over jobs of $\tau_{i+1}$, for all $i$.)

With respect to a given platform, a given sporadic task system is said to be feasible if there exists a schedule meeting all deadlines, for every collection of jobs that may be generated by the task system. A given sporadic task system is said to be (global) DM schedulable if DM meets all deadlines for every collection of jobs that may be generated by the task system. While every DM-schedulable task system is trivially feasible, it is known that not all feasible task systems are DM-schedulable. A schedulability test for global DM accepts as input the specifications of a sporadic task system and an identical multiprocessor platform, and determines whether the system is global-DM schedulable upon the platform. Such a test is exact if it correctly identifies all schedulable systems, and sufficient if it identifies some, but not necessarily all, schedulable systems (however, it must not incorrectly declare some non schedulable system to be global-DM schedulable).

Processor speedup factor. Processor speedup factors represent a quantitative approach towards comparing different sufficient schedulability tests. A schedulability test is defined to have a processor speedup factor $f$, $f \geq 1$, if any task system not deemed to be schedulable by this test upon a particular platform is guaranteed to not be feasible — schedulable by an optimal clairvoyant scheduler — upon a platform in which each processor is at most $1/f$ times as fast. More formally,

Definition 1 (Processor speedup factor) A schedulability test has a processor speedup factor $f$, $f \geq 1$, if it is guaranteed that any task system that is feasible upon a specified platform is deemed to be schedulable by the test upon a platform in which each processor is at least $f$ times as fast.

Intuitively, the processor speedup factor of a schedulability test quantifies both the pessimism of the test and the non-optimality of the scheduling algorithm. According to this metric, schedulability tests with smaller processor speedup factors are superior to ones with larger processor speedup factors, with a processor speedup factor equal to one implying both that the scheduling algorithm is optimal and that the test is exact.

It is reasonable to ask whether it makes sense to bundle both the non-optimality of the scheduling algorithm and the pessimism of the test into a single metric. From a pragmatic perspective, we believe that the answer is “yes” for our domain of interest, which is hard-real-time systems. Since it must be a priori guaranteed in such hard-real-time systems that all deadlines will be met during run-time, a scheduling algorithm is only as good as its associated schedulability test. In other words, a scheduling algorithm, no matter how close to optimal, cannot be used in the absence of an associated schedulability test able to guarantee that all deadlines will be met; what matters is that the combination of scheduling algorithm and schedulability test together have desirable properties.

Our contributions. The main contribution contained in this paper is a new sufficient schedulability test (Theorem 4) for the global DM scheduling of constrained-deadline sporadic task systems. We prove (Section 5) that our test has a superior processor speedup factor when compared to previously-proposed tests (some of these prior tests are described in Section 3). We also bound the amount by which our schedulability test may deviate from a hypothetical optimal test, by proving a lower bound on the processor speedup factor of any sufficient schedulability test for global DM.

Organization. The remainder of this paper is organized as follows. In Section 2 we present an abstraction for quantifying the computational demand of a sporadic task system. In Section 3, we present some related work that is particu-
larly relevant to the work we are doing here. In Section 4 we derive our new sufficient schedulability test, and in Section 5 we derive its processor speedup factor: these sections are the technical heart of the paper. In Section 6 we obtain a lower bound on the processor speedup factor of any global-DM schedulability test, by constructing a feasible task system that is not schedulable using global DM unless all processors are sped up by a certain minimum amount.

## 2 The forced-forward demand bound function

In order to devise schedulability tests, it is necessary to quantify the cumulative execution requirement that sequences of jobs may place on a computing platform. Given a sequence of jobs $J$ and a specified time interval $[t_a, t_f]$, the demand of $J$ over $[t_a, t_f]$ is defined to be the sum of the execution requirements of all jobs in $J$ with arrival times $\geq t_a$ and deadlines $\leq t_f$. The demand bound function $DBF(\tau, t)$ of sporadic task system $\tau$ for an interval length $t$ is then defined to be the largest demand by any legal sequence of jobs that may be generated by $\tau$ over any interval of length $t$.

However, Baker and Cirinei [1] observed that even jobs whose scheduling windows only intersect partially with an interval can require computation within the interval and hence contribute to the computational demand within it. They introduced a notion that they call minimum demand, which refines the demand concept as defined above. The minimum demand of a given collection of jobs in any specific time interval is the minimum amount of execution that the sequence of jobs could require within that interval in order to meet all its deadlines. An essentially identical concept was independently proposed in [7], and called the necessary demand; a related concept called forced-forward demand was also introduced. This metric was further generalized in [3], to the form discussed below — this metric is general enough to allow for the specification that the execution may be occurring on speed-$\sigma$ processors, for arbitrary $\sigma > 0$.

Let $\tau_i$ denote a sporadic task, $t$ any positive real number, and $\sigma$ any positive real number $\leq 1$. The forced forward demand bound function $\text{FF-DBF}(\tau_i, t, \sigma)$ is defined as follows:

\[
\text{FF-DBF}(\tau_i, t, \sigma) \defeq \begin{cases} 
q_i C_i + \frac{C_i}{t/T_i} & \text{if } r_i \geq D_i \\
q_i C_i - (D_i - r_i)\sigma & \text{if } D_i > r_i \geq D_i - \frac{C_i}{\sigma} \\
0 & \text{otherwise}
\end{cases}
\]  

(1)

where

\[
q_i \defeq \left\lfloor \frac{t}{T_i} \right\rfloor \quad \text{and} \quad r_i \defeq t \mod T_i,
\]

Informally speaking, $\text{FF-DBF}(\tau_i, t, \sigma)$ can be thought of as a bound on the demand of $\tau_i$ for interval-length $t$, when execution outside the interval occurs on a speed-$\sigma$ processor. This function is illustrated for an example task in Figure 1.

As with DBF, the FF-DBF concept extends from individual tasks to task systems as well; for a task system $\tau$:

\[
\text{FF-DBF}(\tau, t, \sigma) \defeq \sum_{\tau_i \in \tau} \text{FF-DBF}(\tau_i, t, \sigma)
\]

(2)

Some additional notation that we will use in this paper. Let $\tau$ denote a task system, and $\tau_k$ any task in $\tau$:

\[
density DENS_{\tau_k} \defeq \frac{C_k}{D_k}.
\]

For the entire task system $\tau$,

\[
\text{Utilization } U(\tau) \defeq \sum_{\tau_i \in \tau} C_i/T_i.
\]

\[
\text{Maximum density } DENS_{\max} \defeq \max_{\tau_i \in \tau} DENS_i.
\]

\[
\text{Hyperperiod } P(\tau) \defeq \text{lcm}_{\tau_i \in \tau}\{T_i\}.
\]

## 3 Prior global-DM schedulability tests

In this section, we briefly review some previously-derived sufficient DM-schedulability tests. We will focus on those tests for which quantitative performance bounds, in the form of processor speedup factors, have been determined. While some other tests have been proposed that have only been evaluated by extensive experimental simulations, such tests are not directly comparable to the new one we will derive here, and hence orthogonal to this paper. The interested reader is referred to Bertogna’s dissertation [6] for a comprehensive discussion of such schedulability tests.

In [9], a sufficient test was derived for determining whether a given sporadic task system is global-DM schedulable upon a preemptive multiprocessor platform comprised of $m$ unit-capacity processors. Assume that the tasks in $\tau$ are indexed in deadline-monotonic order ($D_i \leq D_{i+1}$ for all $i$), and let
Theorem 1 (from [9]) Sporadic task system $\tau$ is global-DM schedulable upon a platform comprised of $m$ unit-capacity processors, provided

$$\text{LOAD}(k) \leq \frac{m - (m - 1)DENS_k}{3}$$

for all $k$, $1 \leq k \leq n$.\[\]

It was shown that the processor speedup factor for this global-DM schedulability test is at most $(4 - \frac{1}{m})$, when implemented upon $m$-processor platforms. This result was subsequently improved [2], and extended to sporadic task systems [4] which are not constrained (i.e., in which individual tasks’ relative deadlines may exceed their periods):

Theorem 2 (from [2, 4]) Sporadic task system $\tau$ is global-DM schedulable upon a platform comprised of $m$ unit-capacity processors, provided

$$\text{LOAD}(k) \leq \max \left( \frac{\mu_k}{3}, \frac{\mu_k - \lfloor \mu_k \rfloor - 1}{2} \right) \max_k$$

for all $k$, $1 \leq k \leq n$, where $\mu_k$ is as defined as follows:

$$\mu_k \equiv m - (m - 1)DENS_k$$

and $DENS_{\text{max}}(k)$ denotes the quantity $\max_{i=1}^{k} \{DENS_i\}$ (recall that we’re assuming that the tasks in $\tau$ are indexed in deadline-monotonic order).\[\]

The processor speedup factor for this improved schedulability test was shown to equal

$$\frac{2(m - 1)}{(4m - 1) - \sqrt{12m^2 - 8m + 1}}$$

This approaches $2\sqrt{3}$ (which is $\approx 3.73$) as $m \to \infty$.

4 An improved sufficient schedulability condition

In this section, we derive a new sufficient schedulability condition for global DM scheduling of constrained-deadline sporadic task systems, that characterizes DM-schedulability in terms of the forced forward demand bound function. Later, we will show that this new test offers superior performance guarantees — i.e., a smaller processor speedup factor — when compared to the test in Theorem 2.

Theorem 3 Suppose that constrained-deadline sporadic task system $\tau$ is not DM-schedulable upon a platform comprised of $m$ unit-capacity processors. For each $s$, $DENS_{\text{max}}(\tau) < s \leq 1$,

$$\frac{\text{LOAD}(\tau, s)}{2} > \frac{m - (m - 1)s}{2}$$

Proof: Suppose that sporadic task system $\tau$ is not DM-schedulable. Let $s \geq DENS_{\text{max}}(\tau)$ denote some value for $\sigma$. We will derive a corresponding $L$ such that $\text{FF-DBF}(\tau, L, s) > (m - (m - 1)s) \times L$.

Consider a minimal sequence of jobs of $\tau$ upon which DM misses deadlines. Let $t_o$ denote the (first) instant at which a deadline miss occurs. Let $j_1$ denote a job that misses its deadline at $t_o$, and let $t_1$ denote $j_1$’s arrival-time.

Since lower-priority jobs have no effect on the scheduling of greater-priority jobs, the minimal sequence of jobs on which DM misses a deadline does not include any job with relative deadline $> (t_o - t_1)$. Hence, we note that no jobs with deadline $> t_o + (t_o - t_1)$ are executed in this minimal schedule.

We define a sequence of jobs $j_i$, time-instants $t_i$, and an index $k$, according to the pseudo-code in Figure 2 (also see Figure 3).
for $i \leftarrow 2, 3, \ldots$ do
  let $j_i$ denote a job that
  - arrives at some time-instant $t_i < t_{i-1}$;
  - has a deadline after $t_{i-1}$;
  - has not completed execution by $t_{i-1}$; and
  - has executed for strictly less than $(t_{i-1} - t_i) \cdot s$ units over the interval $[t_i, t_{i-1})$.

if there is no such job then
  $k \leftarrow (i - 1)$
  break (out of the for loop)
end if
end for

Figure 2. Proof of Theorem 3: defining the $j_i$’s, the $t_i$’s and $k$.

Let $L$ denote the length of the interval $[t_k, t_o + (t_o - t_1))$:

$$L \triangleq (t_0 - t_k) + (t_o - t_1) = 2t_o - t_1 - t_k$$

Since $t_k \leq t_1$, we observe that $(t_o - t_k)$ is at least $(t_o - t_1)$ and must

$$(t_o - t_k) \geq L/2 \quad (7)$$

For each $i, 1 \leq i \leq k$, let $W_i$ denote the total amount of execution that occurs over the interval $[t_i, t_{i-1})$.

Lemma 3.1 \text{FF-DBF}(\tau, L, s) \geq \sum_{i=1}^{k} W_i.$$

Proof: All jobs that execute in $[t_k, t_o)$ (and hence contribute to $\sum_{i=1}^{k} W_i$) have their deadlines within the interval $[t_k, 2t_o - t_1)$. Some of them will also have arrived within this interval, while others may not.

Now it may be verified that the amount of execution that jobs of any task $\tau_i$ contribute to $\sum_{i=1}^{k} W_i$ is bounded from above by the scenario in which a job of $\tau_i$ has its deadline coincident with the end of the interval, and prior jobs have arrived exactly $T_k$ time-units apart. Under this scenario, the jobs of $\tau_i$ that may contribute to $\sum_{i=1}^{k} W_i$ include

- at least $q_i \triangleq \lceil L/T_k \rceil$ jobs of $\tau_i$ that lie entirely within the interval $[t_k, 2t_o - t_1)$; and
- (perhaps) an additional job that has its deadline at time-

We now consider two separate cases:

1. $r_{t_i} \geq D_i$; i.e., the additional job with deadline at $t_k + r_{t_i}$ arrives at or after $t_k$. In this case, its contribution is $C_i$.

2. $r_{t_i} < D_i$; i.e., the additional job with deadline at $t_k + r_{t_i}$ arrives prior to $t_k$. From the exit condition of the for-loop, it must be the case that this job has completed at least $(D_e - r_{t_i}) \times s$ units of execution prior to time-instant $t_k$; hence, its remaining execution is at most $\max(0, C_i - (D_t - r_{t_i}) \times s)$.

In either case, it may be seen that the upper bound on the total contribution of $\tau_i$ to $\sum_{i=1}^{k} W_i$ is equal to $\text{FF-DBF}(\tau_i, L, s)$ (see Equation 1). The lemma follows, by summing over all tasks $\tau_i \in \tau$. ■

Lemma 3.2 For each $i, 1 \leq i \leq k$,

$$W_i \geq ((m - (m - 1)s) \times (t_{i-1} - t_i)$$

Proof: Let $x$ denote the total length of the time-intervals over $[t_i, t_{i-1})$ during which job $j_i$ executes. By choice of job $j_i$, it is the case that

$$x < (t_{i-1} - t_i) \cdot s$$

By choice of job $j_i$, it has not completed execution by time-

Hence over $[t_i, t_{i-1})$, all $m$ processors must be executing whenever $j_i$ is not; it follows that

$$W_i \geq m(t_{i-1} - t_i - x) + x = m(t_{i-1} - t_i) - (m - 1)x > m(t_{i-1} - t_i) - (m - 1)(t_{i-1} - t_i)s = (m - (m - 1)s) \times (t_{i-1} - t_i)$$

and the lemma is proved. ■
We now observe that
\[
\sum_{i=1}^{k} W_i \\
n > \sum_{i=1}^{k} \left( (m - (m - 1)s) \times (t_{i-1} - t_i) \right) \text{ (By Lemma 3.2)} \\
n = (m - (m - 1)s) \times \sum_{i=1}^{k} (t_{i-1} - t_i) \\
n = (m - (m - 1)s) \times (t_o - t_k) \\
n \geq (m - (m - 1)s) \times \frac{L}{2} \text{ (By Equation 7)}
\]

By Lemma 3.1, we therefore have
\[
\text{FF-DBF} \left( \tau, L, s \right) > (m - (m - 1)s) \times \frac{L}{2} \\
\Rightarrow \frac{\text{FF-DBF} \left( \tau, L, s \right)}{L} > \frac{1}{2} (m - (m - 1)s) \\
\Rightarrow \text{FF-LOAD} \left( \tau, s \right) > \frac{m - (m - 1)s}{2}
\]

and Theorem 3 is proved. ■

Theorem 3 above asserts that, for any DM-schedulable \( \tau \), there is some \( s \geq \text{DENS}_{\text{max}}(\tau) \) for which Condition 6 is violated. To determine whether a given \( \tau \) is global DM-schedulable, therefore, our approach would be to methodically search through the range \([\text{DENS}_{\text{max}}(\tau), 1] \) of candidate values for \( s \), seeking to determine whether there is any such \( s \) that causes Condition 6 to be violated and thereby validates the schedulability of \( \tau \). (A similar search in the context of global-EDF schedulability is detailed in [3] — as can be seen from perusing that paper, the search is somewhat non-trivial.)

However, our objective in this paper is to obtain global-DM schedulability test with the best possible processor speedup factor. For this purpose, it turns out that the following specialized form of Theorem 3 suffices. (This is a “specialization” of Theorem 3 in the sense that it is essentially only checking whether Condition 6 is violated at the single value \( s = \text{DENS}_{\text{max}}(\tau) \); therefore, while all systems deemed schedulable by Theorem 4 will also be deemed schedulable by Theorem 3, the converse is not true: there are task systems determined to be schedulable according to Theorem 3 that Theorem 4 will not deem schedulable — those for which Condition 6 is only violated for values of \( s \) other than \( \text{DENS}_{\text{max}}(\tau) \).)

**Theorem 4** Any constrained-deadline sporadic task system \( \tau \) satisfying
\[
\text{FF-LOAD}(\tau, \text{DENS}_{\text{max}}(\tau)) \leq \frac{1}{2} \left( m - (m - 1)\text{DENS}_{\text{max}}(\tau) \right)
\]
is guaranteed to be DM-schedulable upon a platform comprised of \( m \) unit-capacity processors.

**Proof:** Immediately follows from Theorem 3, by instantiating the variable \( s \) in Inequality 6 to \( \text{DENS}_{\text{max}}(\tau) \). ■

5 A speedup result

First, we observe that a necessary multiprocessor feasibility condition can be derived in terms of \( \text{FF-LOAD} \):

**Lemma 1** If \( \text{FF-LOAD}(\tau, s) > ms \) then \( \tau \) is not feasible on \( m \) speed-s processors.

**Proof:** Suppose that \( \text{FF-LOAD}(\tau, s) > ms \). Let \( t_o \) denote a value for \( t \) that maximizes the RHS of Equation 2:
\[
\text{FF-LOAD}(\tau, s) = \frac{\sum_{t \in \tau} \text{FF-DBF}(t, t_o, s)}{t_o}
\]

By definition of \( \text{FF-DBF} \), each task \( t_\ell \) can generate a sequence of jobs that together require \( \text{FF-DBF}(t_\ell, t_o, s) \) units of execution over some interval of length \( t_o \), when executing upon speed-s processors. Since the different tasks of a sporadic task system are assumed to be independent of each other, such intervals for the different tasks can be aligned; the total execution requirement by all the tasks over
Theorem 4 has a processor speedup factor equal to

\[ \text{Lemma 2} \, \text{If task system } \tau \text{ fails the DM schedulability test of Theorem 4, then it is not feasible upon a platform comprised of } m \text{ speed-} \frac{m}{3m-1} \text{ processors.} \]

\[ \text{Proof:} \] Suppose that \( \tau \) fails the EDF schedulability test of Theorem 4.

If \( \text{DENS}_{\text{max}}(\tau) > \frac{(m/(3m-1)} \) then \( \tau \) is trivially not feasible upon a platform comprised of \( (m/(3m - 1)) \) processors, and we are done.

Assume now that \( \text{DENS}_{\text{max}}(\tau) \leq \frac{(m/(3m-1))} \). Since \( \tau \) fails the DM schedulability test of Theorem 4, it must be the case that

\[ \text{FF-LOAD}(\tau, \text{DENS}_{\text{max}}(\tau)) > \frac{1}{2} \left( m - (m - 1) \frac{m}{3m-1} \right) \]

\[ \frac{3m^2 - m - m^2 + m}{2(3m - 1)} \]

\[ = \frac{m}{3m-1} \]

\[ \geq m \text{ DENS}_{\text{max}}(\tau) \]

It therefore follows from Lemma 1 above that \( \tau \) is not feasible upon a platform comprised of \( m \) speed-DENS_{\text{max}}(\tau) processors. Since \( \text{DENS}_{\text{max}}(\tau) \) is assumed to be \( \leq \frac{(m/(3m-1))} \), it is therefore not feasible upon a platform comprised of \( m \) speed-\( \frac{m}{3m-1} \) processors. \[ \blacksquare \]

By taking the contrapositive of Lemma 2 above and observing that \( 1/(\frac{m}{3m-1}) \) is equal to \( 3 - \frac{1}{m} \), we have

\[ \text{Theorem 5} \, \text{The global-DM sufficient schedulability test of Theorem 4 has a processor speedup factor equal to } [3 - (1/m)]. \]

\[ \blacksquare \]

The bound of Theorem 5 approaches 3 as \( m \to \infty \). Since the best previously-known speedup factor (Equation 5) approaches \( \approx 3.73 \), we see that this asymptotically represents a significant improvement.

6 A lower bound on processor speedup factor

We have seen, in Theorem 5 above, that our sufficient schedulability test has a processor speedup factor of \( (3 - (1/m)) \). In this section, we will obtain a lower bound on the processor speedup factor of any global-DM schedulability test. To do so, we will now construct a sporadic task system and show that it is both

1. feasible on a platform comprised of \( m \) speed-\((x + \epsilon)\) processors where \( \epsilon \) is an arbitrarily small positive number while the precise value of \( x \) will be specified later; and
2. not schedulable using global-DM on a platform comprised of \( m \) speed-1 processors.

Taken together, these facts imply a lower bound of \( \frac{1}{\epsilon} \) (more precisely, \( \lim_{x \to 0} \frac{1}{x + \epsilon} \)) on the processor speedup factor of any global-DM schedulability test.

\[ \section{The task system.} \]

Our technique for constructing the desired task system is inspired by a similar construction in [8]. Let \( x \) denote a constant, \( 0 < x < \frac{1}{2} \), whose value we will derive later. Let \( n \) denote a very large positive integer \( (n \to \infty) \).

Let \( k \) be defined as follows (since \( n \) is assumed to \( \to \infty \), observe that \( k \to 0 \)):

\[ k \geq 2n - 2 \]

\[ 1 - x \]

For each \( j, 1 \leq j < n \), let

\[ T_j \equiv x + \frac{n + j - 2}{k} \]

Let \( \tau \) denote the sporadic task system comprised of the following \( m(n - 1) + 1 \) tasks:

- For each \( j, 1 \leq j < n \), there are \( m \) tasks each with execution requirement \( 1/k \), and relative deadline and period both equal to \( T_j \).
- There is one task with relative deadline and period both equal to one, with execution requirement greater than \( x \) by an arbitrarily small amount.

\[ 1 \text{This value will turn out to depend on the number of processors } m; \]

for \( m \to \infty \), \( x = (W(2e) - 1) \approx 0.3748 \), where \( W(t) \) denotes the well-known Omega function. This value of \( x \) is derived in the remainder of this section.
Observe that by the definition of the $T_j$'s,

$$
T_1 = \frac{1}{2} + \frac{x}{2k} \\
T_{j+1} = T_j + \frac{1}{k} \text{ for all } j, \text{ and} \\
T_{n-1} = 1 - \frac{1}{k}
$$

§2. Demonstrating DM-unschedulability. Suppose that each task has its first job arrive at time-instant 0, and subsequent jobs as soon as legally permitted to do so. According to DM priorities, the $m(n-1)$ jobs each with execution-requirement $1/k$ will be assigned greater priority; together, their execution requirement is

$$(m(n-1) \times \frac{1}{k}) = (m(n-1) \times \frac{1-x}{2n-2}) = (m \times \frac{1-x}{2})$$

Hence, these jobs execute on all $m$ processors over the interval $[0, \frac{1-x}{2}]$. The earliest that the 2nd job of any of these high-priority tasks arrives is $T_1 = \frac{1}{2} + \frac{x}{2}$; hence over the interval $[\frac{1-x}{2}, \frac{1+x}{2})$, which is of length $x$, the only executing job is the job of the lowest-priority task while the remaining $(m-1)$ processors are idled. Since its execution requirement exceeds $x$, this lowest-priority job has not completed execution at time-instant $(\frac{1+x}{2})$ when more higher-priority jobs arrive.

Since exactly $m$ jobs with period $T_j$ and execution-requirement $\frac{1}{k}$ arrive at each $T_j$, $1 \leq j \leq n-1$, and successive $T_j$'s are exactly $\frac{1}{k}$ time units apart, these higher-priority jobs completely consume the processor over the interval $[\frac{1+x}{2}, 1)$. Therefore the lowest-priority job receives no further execution prior to its deadline at time-instant 1, and consequently misses its deadline (and thus bears witness to the non-schedulability of this task system $\tau$ by global-DM).

§3. Computing $\tau$’s utilization. In order to determine the utilization $U(\tau)$ of the task system $\tau$ specified above, let us first compute the total utilization of all the tasks other than the lowest-priority one:

$$
\sum_{j=1}^{n-1} \left( \frac{C_j}{T_j} \right)
= \sum_{j=1}^{n-1} \frac{m \times \frac{1}{k}}{x + (n+j-2)/k} \\
= \sum_{j=1}^{n-1} \frac{mx + n + j - 2}{kx + n + j - 2} \\
= \sum_{j=1}^{n-1} \frac{m}{\frac{2n-2}{1-x} x + n + j - 2} \\
\leq m \int \frac{\frac{2n-2}{1-x} x + 2n-3}{\frac{2n-2}{1-x} x + n-1} \frac{1}{x-1} dx 
$$

(See Figure 4)

$$
= m \ln \left( \frac{2n-2}{1-x} x + 2n-4 \right) - m \ln \left( \frac{2n-2}{1-x} x + n-2 \right) \\
= m \ln \left( \frac{(2n-2)x + (2n-4)(1-x)}{(2n-2)x + (n-2)(1-x)} \right) \\
= m \ln \left( \frac{2n + 2x - 4}{n(1+x) - 2} \right)
$$

As $n$ is made arbitrarily large ($n \rightarrow \infty$), this utilization bound approaches $m \ln \left( \frac{2}{1+x} \right)$.

The utilization of the entire task system is obtained by adding the lowest-priority task’s utilization (which exceeds $x/1$ by an arbitrarily small amount) to this; i.e., $U(\tau)$ exceeds $\left( x + m \ln \left( \frac{2}{1+x} \right) \right)$ by an arbitrarily small amount.

§4. Obtaining the processor speedup factor. First, we determine the platform comprised of $m$ processors of the slowest speeds, on which $\tau$ is feasible.

Lemma 3 Task system $\tau$ is feasible upon a platform comprised of $m$ speed-$(x + \epsilon)$ processors, where $x$ satisfies

$$
x(1 - \frac{1}{m}) = \ln \left( \frac{2}{1+x} \right)
$$

and $\epsilon$ is an arbitrary small positive number.

Proof: Observe that each task in the task system $\tau$ that we have constructed has its relative deadline and period parameters equal to each other. It is well known that such “implicit-deadline” systems are feasible on multiprocessor platforms provided the processor speed is $\geq \text{DENS}_{\text{max}}(\tau)$ and the total capacity of the platform is $\geq U(\tau)$. Now, $\text{DENS}_{\text{max}}(\tau)$ is equal to the density of the lowest-priority task (since the densities of all other tasks is arbitrarily small as $n \rightarrow \infty$), and hence exceeds $x$ by an arbitrarily small amount. Therefore $\tau$ is feasible on a platform comprised of $m$ processors of speed exceeding $x$ by an arbitrarily small
The area beneath the jagged line between $a$ and $b$ on the $x$-axis equals the summation; the area beneath the curved line between $a$ and $b$ on the $x$-axis equals $\int_a^b \frac{1}{x+1} \, dx$.

Table 1. Values of $x$ that satisfy Equation 9 for selected values of $m$.

<table>
<thead>
<tr>
<th>$m$</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
<th>10</th>
</tr>
</thead>
<tbody>
<tr>
<td>$x$</td>
<td>0.532</td>
<td>0.466</td>
<td>0.439</td>
<td>0.424</td>
<td>0.398</td>
</tr>
</tbody>
</table>

amount, where $x$ satisfies

\[
mx = x + m \ln\left(\frac{2}{1+x}\right) = x(1 - (1/m)) = \ln\left(\frac{2}{1+x}\right)
\]

as claimed in the statement of the lemma. ■

We have therefore constructed a task system that is not schedulable using global-DM on $m$ unit-speed processors but is feasible on $m$ processors each of speed just a bit more than $x$, where $x$ is as defined by Equation 9. It hence follows that

**Theorem 6** No global-DM schedulability test can have a processor speedup factor smaller than $1/x$, where $x$ is as defined by Equation 9.

Equation 9 can be solved numerically for different fixed values of $m$; some of these solutions are presented in Table 1. For very large $m$, $1/m$ becomes very small; hence as $m \to \infty$ the value of $x$ is asymptotically equal to the solution of the equation

\[
x = \ln\left(\frac{2}{1+x}\right) = e^x = \frac{2}{1+x} = (1+x)e^{1+x} = 2e
\]

from which we conclude that

\[
x = W(2e) - 1,
\]

where $W(t)$ denotes the Lambert $W$-function (also known as the Omega function). Mathematical tables tell us that $W(2e) \approx 1.3748$; i.e., $x$ asymptotically approaches 0.3748 as the number of processors increases. That is, for $m \to \infty$, $(1/0.3748) \approx 2.668$ — is a lower bound on the processor speedup factor of any global-DM schedulability test. By contrast, as $m \to \infty$ the processor speedup factor of the test of Theorem 4 is 3.

**7 Conclusions**

We have derived a new sufficient schedulability test (Theorem 4) for determining whether a given constrained-deadline sporadic task system is DM-schedulable upon
a preemptive multiprocessor platform, when global inter-
processor migration is permitted. We have shown that the
processor speedup factor for our test is equal to $(3 - (1/m))$,
thereby demonstrating that any constrained-deadline spo-
radic task system that is feasible upon an $m$-processor mul-
tiprocessor platform is correctly identified by our test as be-
ing DM-schedulable upon a platform in which each proces-
sor is $(3 - (1/m))$ times as fast. We have also computed
a lower bound on the processor speedup factor of any suf-
cient schedulability test for global DM, thereby bounding
the degree by which our test deviates from a hypothetical
optimal one.

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