COMP 520  Compilers
Written Assignment 6

Consider the compilation of the following (miniJava) expression for a register-oriented processor architecture like MIPS (here variables $x$ and $y$ are int variables stored in memory)

$$(x + y) \times (x + y)$$

(a) (on back) Show an AST for this expression and use Sethi-Ullman numbering on each node to determine the minimum number of registers needed to evaluate the expression. How many registers are needed?

(b) (on back) Show the tuple code generated by simple code generation using temporaries.

(c) (on back) Show how the tuple code in (b) can be simplified to the code in (d) using common subexpression elimination.

(d) Determine the lifetimes of each temporary $t_1 – t_5$.

\[
\begin{array}{ccccc}
    & t_1 & t_2 & t_3 & t_4 & t_5 \\
\hline
    t_1 & := x \\
    t_2 & := y \\
    t_3 & := t_1 + t_2 \\
    t_4 & := t_3 \times t_1 \\
    t_5 & := t_4 + t_3 \\
\end{array}
\]

(e) What is the minimum number of registers $k$ needed to evaluate the tuple code above? Show an assignment of temporaries to $k$ registers by constructing the interference graph and coloring it using $k$ colors.