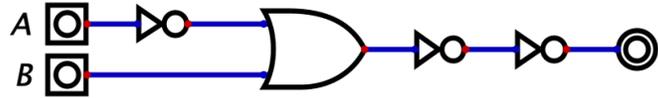


**Question 1: Minimizing Transistor Count** Minimize the number of transistors in this circuit by redesigning it to use only NAND gates and inverters. Compare the number of transistors in the original circuit and the new circuit:



**Question 2: Minimizing Transistor Count 2** Minimize the number of transistors in this circuit by redesigning it to use only NAND gates and inverters. Compare the number of transistors in the original circuit and the new circuit:

