

Threading review

Threading review

What is threading?

- Multiple threads of execution in one address space

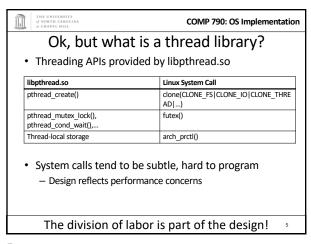
- x86 hardware:

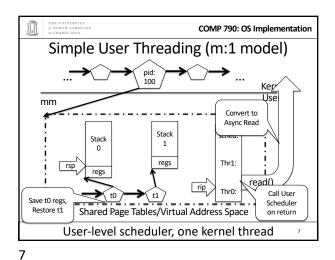
One cr3 register and set of page tables shared by 2+ different register contexts otherwise (rip, rsp/stack, etc.)

- Linux:

One mm_struct shared by several task_structs

- Does JOS support threading?





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User Threading Observations

One can easily switch stacks in user-space

No privileged instructions needed

Same for saving and restoring PC (rip)

Convert blocking to non-blocking calls

OS must provide non-blocking equivalents

Transparent help from libc

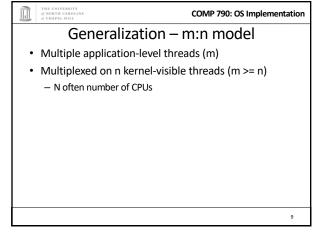
Catch futexes, yield

Add O_ASYNC to open, detect when data ready

Need a second, user-level thread scheduler

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User Threading Complexity

• Lots of libc/libpthread changes

- Working around "unfriendly" kernel API

• Bookkeeping gets much more complicated

- Second scheduler

- Synchronization different

• Can do crude preemption using:

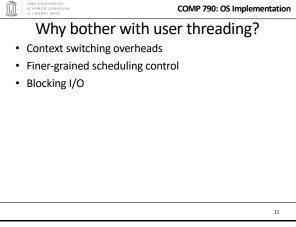
- Certain functions (locks)

- Timer signals from OS

- Signals

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Context Switching Overheads

Recall: Forking a thread halves your time slice

Takes a few hundred cycles to get in/out of kernel

Plus cost of switching a thread

Time in the scheduler counts against your timeslice

2 threads, 1 CPU

If I can run the context switching code locally (avoiding trap overheads, etc), my threads get to run slightly longer!

Stack switching code works in userspace with few changes

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Blocking I/O

· I have 2 threads, they each get half of the

- If A blocks on I/O and B is using the CPU

- A's quantum is "lost" (at least in some schedulers)

- Maybe application cares more about B's CPU time...

Scheduler Activations

Some BSDs support(ed) scheduler activations

· On any blocking operation, kernel upcalls back to

User scheduler keeps kernel notified of how many

- Kernel allocates up to that many scheduler activations

runnable tasks it has (via system call)

· Better API for user-level threading

Not available on Linux

Eliminates most libc changes
 Easier notification of blocking events

user scheduler

application's quantum

- B gets half the CPU time

Modern Linux scheduler:

- A gets a priority boost



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Finer-Grained Scheduling Control

- Example: Thread 1 has a lock, Thread 2 waiting for lock
 - Thread 1's quantum expired
 - Thread 2 just spinning until its quantum expires
 - Wouldn't it be nice to donate Thread 2's quantum to Thread 1?
 - Both threads will make faster progress!
- Similar problems with producer/consumer, barriers, etc.
- Deeper problem: Application's data flow and synchronization patterns hard for kernel to infer

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Blocking I/O and Events

- Events: abstraction for dealing with blocking I/O
- · Layered over a user-level scheduler
- Lots of literature on this topic if you are interested...

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What is a scheduler activation?

- Like a kernel thread:
 - A kernel stack and a user-mode stack
 - Represents the allocation of a CPU time slice
- Not like a kernel thread:
 - Does not automatically resume a user thread
 - Goes to one of a few well-defined "upcalls"
 - New timeslice, Timeslice expired, Blocked SA, Unblocked SA
 - Upcalls must be reentrant (called on many CPUs at same time)
 - User scheduler decides what to run

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Downsides of scheduler activations

- A random user thread gets preempted on every scheduling-related event
 - Not free!
 - User scheduling must do better than kernel by a big enough margin to offset these overheads
- Moreover, the most important thread may be the one to get preempted, slowing down critical path
 - Potential optimization: communicate to kernel a preference for which activation gets preempted to notify of an event

Optional Reading on Scheduler Activations



Back to NPTL

- Ultimately, a 1:1 model was adopted by Linux.
- Whv?
 - Higher context switching overhead (lots of register copying and upcalls)
 - Difference of opinion between research and kernel communities about how inefficient kernel-level schedulers are. (claims about O(1) scheduling)
 - Way more complicated to maintain the code for m:n model. Much to be said for encapsulating kernel from thread library!

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Meta-observation

- Much of 90s OS research focused on giving programmers more control over performance
 - E.g., microkernels, extensible OSes, etc.
- Argument: clumsy heuristics or awkward abstractions are keeping me from getting full performance of my hardware
- · Some won the day, some didn't
 - High-performance databases generally get direct control over disk(s) rather than go through the file system

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User-threading in practice

- Has come in and out of vogue
 - Correlated with how efficiently the OS creates and context switches threads
- Linux 2.4 Threading was really slow
 - User-level thread packages were hot
- Linux 2.6 Substantial effort went into tuning threads
 - E.g., Most JVMs abandoned user-threads

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Other issues to cover

- Signaling
 - Correctness
 - Performance (Synchronization)
- · Manager thread
- · List of all threads
- · Other miscellaneous optimizations

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What was all the fuss about signals?

- 2 issues:
 - 1) The behavior of sending a signal to a multi-threaded process was not correct. And could never be implemented correctly with kernel-level tools (pre 2.6)
 - Correctness: Cannot implement POSIX standard
 - Signals were also used to implement blocking synchronization. E.g., releasing a mutex meant sending a signal to the next blocked task to wake it up.
 - Performance: Ridiculously complicated and inefficient

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Issue 1: Signal correctness w/ threads

- Mostly solved by kernel assigning same PID to each thread
 - 2.4 assigned different PID to each thread
 - $\boldsymbol{-}$ Different TID to distinguish them
- Problem with different PID?
 - POSIX says I should be able to send a signal to a multithreaded program and any unmasked thread will get the signal, even if the first thread has exited
- To deliver a signal kernel has to search each task in the process for an unmasked thread

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Issue 2: Performance

- · Solved by adoption of futexes
- Essentially just a shared wait queue in the kernel
- · Idea:
 - Use an atomic instruction in user space to implement fast path for a lock (more in later lectures)
 - If task needs to block, ask the kernel to put you on a given futex wait queue
 - Task that releases the lock wakes up next task on the futex wait queue
- See optional reading on futexes for more details

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Manager Thread

- A lot of coordination (using signals) had to go through a manager thread
 - E.g., cleaning up stacks of dead threads
 - Scalability bottleneck
- Mostly eliminated with tweaks to kernel that facilitate decentralization:
 - The kernel handled several termination edge cases for threads
 - Kernel would write to a given memory location to allow lazy cleanup of per-thread data

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List of all threads

- A pain to maintain
- Mostly eliminated, but still needed to eliminate some leaks in fork
- Generation counter is a useful trick for lazy deletion
 - Used in many systems
 - Idea: Transparently replace key "Foo" with "Foo:0". Upon deletion, require next creation to rename "Foo" to "Foo:1". Eliminates accidental use of stale data.

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Other misc. optimizations

- On super-computers, were hitting the 8k limit on segment descriptors
- Where does the 8k limit come from?
 - Bits in the segment descriptor. Hardware-level limit
- How solved?
 - $\boldsymbol{-}$ Essentially, kernel scheduler swaps them out if needed
 - Is this the common case?
 - No, expect 8k to be enough

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Optimizations

- Optimized exit performance for 100k threads from 15 minutes to 2 seconds!
- PID space increased to 2 billion threads
 - /proc file system able to handle more than 64k processes

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Results

• Big speedups! Yay!

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Summary

- Nice paper on the practical concerns and trade-offs in building a threading library
 - I enjoyed this reading very much
- Understand 1:1 vs. m:n model
 - User vs. kernel-level threading
- Understand other key implementation issues discussed in the paper

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